# General purpose (dual digital transistors) EMH9 / UMH9N / IMH9A

### Features

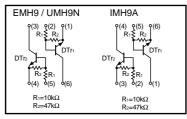
- 1) Two DTC114Ys chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

## ●Structure

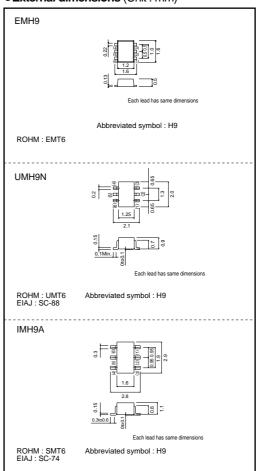
Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr1 and DTr2.

# ●Equivalent circuit



### ●External dimensions (Unit : mm)



## Packaging specifications

	Package	Taping		
	Code	T2R	TN	T110
Type	Basic ordering unit (pieces)	8000	3000	3000
ЕМН9		0	-	-
UMH9N		_	0	-
IMH9A		-	-	0

# ● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	V	
		VIIN	-6		
Output current		lo	70	mA	
		IC (Max.)	100	l IIIA	
Power dissipation	EMH9,UMH9N	Pd	150 (TOTAL)	*1 mW	
	IMH9A	Fu	300 (TOTAL)	*2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

<sup>\*1 120</sup>mW per element must not be exceeded.

# ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
lanut valtage	V <sub>I</sub> (off)	_	-	0.3	V	Vcc=5V, Io=100μA	
Input voltage	VI (on)	1.4	-	_		Vo=0.3V, Io=1mA	
Output voltage	Vo (on)	_	0.1	0.3	V	Io/I=5mA/0.25mA	
Input current	lı	_	-	0.88	mA	V <sub>I</sub> =5V	
Output current	lo (off)	_	-	0.5	μА	Vcc=50V, Vi=0V	
DC current gain	Gı	68	-	_	_	Vo=5V, Io=5mA	
Transition frequency	f⊤	_	250	_	MHz	Vc=10V, I=-5mA, f=100MHz *	
Input resistance	R <sub>1</sub>	7	10	13	kΩ	-	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	3.7	4.7	5.7	_	-	

<sup>\*</sup> Transition frequency of the device

## •Electrical characteristic curves

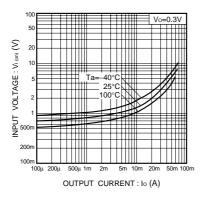


Fig.1 Input voltage vs. output current (ON characteristics)

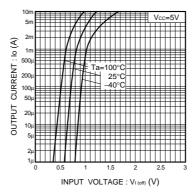


Fig.2 Output current vs. input voltage (OFF characteristics)

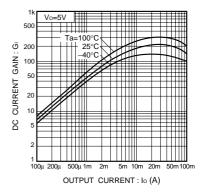


Fig.3 DC current gain vs. output current

<sup>\*2 200</sup>mW per element must not be exceeded.

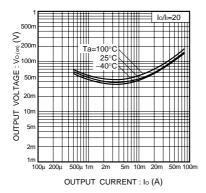


Fig.4 Output voltage vs. output

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